

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Doan et al.

**Serial No.:** 10/644,685

**Filed:** August 20, 2003

**For:** CONTROLLABLE OVONIC PHASE-  
CHANGE SEMICONDUCTOR MEMORY  
DEVICE (as amended)

**Confirmation No.:** 6457

**Examiner:** O. Nadav

**Group Art Unit:** 2811

**Attorney Docket No.:** 2269-3414.7US  
(97-1039.07/US)

**VIA ELECTRONIC FILING**  
**June 25, 2007**

**FEE ADDRESSEE FOR RECEIPT OF PTO NOTICES  
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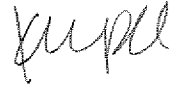
1. This letter is to specify that the FEE ADDRESSEE for this patent is:  

MICRON TECHNOLOGY, INC.  
Mail Stop 525  
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2. The Customer Number for the Fee Addressee is **26809**.

**Serial No. 10/644,685**

3. Any prior FEE ADDRESSEE for this patent is hereby revoked.
4. It is certified that the person whose signature appears below has the authority to change the FEE ADDRESSEE for this patent.

Respectfully submitted,



Krista Weber Powell  
Registration No. 47,867  
Attorney for Applicants  
TRASKBRITT  
P.O. Box 2550  
Salt Lake City, Utah 84110-2550  
Telephone: 801-532-1922

Date: June 25, 2007  
KWP/dlm:lmh

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